

ABSTRACT

A voltage-controlled oscillator includes an oscillating stage with two coupled CMOS inverters forming a quadrupole with two inputs and with two outputs, and two oscillating circuits placed
5 respectively between the inputs and the outputs of the inverters and each having an inductor, the quadrupole being designed so that the outputs of the quadrupole are in phase. The inductors of the oscillating circuits are produced in MOS technology and are superposed one on top of the other.

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